

WEST Search History

DATE: Thursday, March 31, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L26	L24 and transistor and (grid\$)	13
<input type="checkbox"/>	L25	L24 and transistor and (grid\$) and (ic or vlsi or asic)	4
<input type="checkbox"/>	L24	(distribut\$ or segment\$) same net\$4 same compress\$3	3700
<input type="checkbox"/>	L23	L21 and ((distribut\$ or segment) same net\$4)	34
<input type="checkbox"/>	L22	L21 and (distribut\$ same net\$4)	18
<input type="checkbox"/>	L21	L20 and (rc or capacitance)	53
<input type="checkbox"/>	L20	L19 and transistor and (ic or vlsi or asic)	95
<input type="checkbox"/>	L19	L17 and net\$4	2927
<input type="checkbox"/>	L18	L17 and net\$3	1637
<input type="checkbox"/>	L17	(power same (grid\$ or net\$4)) same compress\$3	3300
<input type="checkbox"/>	L16	L15 and (ic or vlsi or asic)	29
<input type="checkbox"/>	L15	l4 and transistor and (power or noise or coupling or grid\$)	64
<input type="checkbox"/>	L14	l4 and hdl	7
<input type="checkbox"/>	L13	L12 and extraction	12
<input type="checkbox"/>	L12	L8 and transistor	31
<input type="checkbox"/>	L11	L8 and transistor and grid\$	2
<input type="checkbox"/>	L10	6247162	7
<input type="checkbox"/>	L9	L8 and transistor and grid\$ and power	2
<input type="checkbox"/>	L8	L4 and (ic or vlsi or asic)	37
<input type="checkbox"/>	L7	L6 and (power or grid\$)	17
<input type="checkbox"/>	L6	L5 and transistor	22
<input type="checkbox"/>	L5	L4 and (ic or vlsi)	27
<input type="checkbox"/>	L4	(rc or capacitance) same net\$4 same compress\$4	147
<input type="checkbox"/>	L3	(rc or capacitance) same net\$ same compress\$4	157
<input type="checkbox"/>	L2	L1 and transistor and grid\$	12
<input type="checkbox"/>	L1	net adj compression	144

END OF SEARCH HISTORY